BIOS 设置 PCIE 兼容模式方法

一、PCIE 16X 插槽兼容模式设置方法

1. 开机按 Delete,进入 BIOS 设置界面,选择 <Chipset> →<System Agent (SA) Configuration> ,按 Enter 键进入该选项。

	Apt	io Setup Ut	ility - Copyri	ght (C)202	3 American Megatrend	ls, Inc.		
Main	Advanced	Chipset	Security	Boot	Save & Exit			
System	System Agent (SA) Configuration							
PCH-IO	Configuration							
						\rightarrow ←: Select Screen \uparrow ↓: Select Item		
						Enter: Select -/+: Change Opt.		
						F1: Genneral Help F2: Previous Values		
						F3: Optimized Defaults F4: Save & Exit Esc: Exit		
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2. 选择 < PEG Port Configuration > , 按 Enter 键进入该选项。

Aptio Setup Util Chipset	ity - Copyright (C)2023 Amer	ican Megatrends, Inc.
System Agent (SA) Configuration		
SA PCIe Code Version VT-d	3.5.0.0 Supported	
 Memory Configuration Graphics Configuration DMI/OPI Configuration PEG Port Configuration 		
Stop Srant Configuration VT-d	[Auto] [Enabled]	 →←: Select Screen ↑↓: Select Item Enter: Select -/+: Change Opt. F1: Genneral Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit Esc: Exit
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3. 选择 < PEG Port Feature Configuration > , 按 Enter 键进入该选项。

Арт	tio Setup Utility Chipset	- Copyright	(C)2023 America	an Megatrend	s, Inc.
PEG Port Configuration					
PEG 0:1:0					
PEG 0:1:1					
PEG 0:1:2					
 PEG Port Feature Config Program PCIe ASPM after Program Static Phase1 E Gen3 Root Port Preset v 	guration er OpROM Eq value for each	[Disabled] [Enabled] Lane			 →←: Select Screen ↑↓: Select Item Enter: Select -/+: Change Opt. F1: Genneral Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit Esc: Exit
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4. 选择 < PEG Port Feature Configuration > 按 Enter 键 , 弹出窗口选择 Enabled , 按 Enter。(PCIE X16 插槽已经设置为兼 容模式)。

Aptio Setup Utility - Copyright (C)2023 American Megatrends,Inc. Chipset					
PEG Port Feature Configuration					
Detect Non-Compliance Device [Disabled]					
Detect Non-Compliance Device Disabled Enabled					
	 →←: Select Screen ↑↓: Select Item Enter: Select -/+: Change Opt. F1: Genneral Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit Esc: Exit 				
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5. 选择完成后按 F4,选择 Yes,按 Enter 保存退出,设置完毕。

Aptio Setup Utility - Copyright Chipset	(C)2023 American Megatrends,Inc.			
PEG Port Feature Configuration				
Detect Non-Compliance Device [Enabled]				
	 → ←: Select Screen ↑↓: Select Item Enter: Select -/+: Change Opt. F1: Genneral Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit Esc: Exit 			
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6. 如需恢复 BIOS 默认状态,开机按 Delete 进入 BIOS 设置界面,按 F3 (恢复 BIOS 初始状态),再按 F4 保存退出即可。

二、其他 PCIE 插槽兼容模式设置方法

1. 开机按 Delete,进入 BIOS 设置界面,选择 <Chipset> →<PCH-IO Configuration> ,按 Enter 键进入该选项。

Aptio Setup Utility - Copyright (C)2023 American Megatrends, Inc.						
Main	Advanced	Chipset	Security	Boot	Save & Exit	
 System PCH-IC 	Agent (SA) Co Configuration	nfiguration	Security			->←: Select Screen ↑↓: Select Item Enter: Select
						-/+: Change Opt. F1: Genneral Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit Esc: Exit
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2. 选择 < PCI Express Configuration > , 按 Enter 键进入该选项。

Aptio	Setup Utility - Copyright (C)2023 Chipset	American Megatrends,Inc.		
 PHC-IO Configuration PCI Express Configuration SATA And RST Configuration USB Configuration Security Configuratin HD Audio Configuration Serial IRQ Mode Restore AC Power Loss PCH LAN Controller DeepSX Power Policies Lan Wake From DeepSx 	n [Continuous] [Power off] [Enabled] [Disabled] [Enabled]	→←: Select Screen ↑↓: Select Item Enter: Select -/+: Change Opt. F1: Genneral Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit Esc: Exit		
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3. 其中 < PCIE2 x4 Slot>、 < PCIE4 x4 Slot>、 < PCIE5 x4 Slot>、 < PCIE3 x4 Slot>,为不同 PCIE 插槽设置项(其中 PCIE2/PCIE4/PCIE5/PCIE3 为主板 PCIE 槽序号),按 Enter 键进入选项。

Aptio Setup Utility - Copyright (C)2023 American Megatrends, Inc.				
	Chipset			
PCI Express Configurat	ion			
PCI Express Clock Gating	[Disabled]			
Legacy IO Low Latency	[Disabled]			
DMI Link ASPM Control	[Disabled]			
PCIE Port assigned to LAN	Disabled			
Port8xh Decode	[Disabled]			
Peer Memory Write Enable	[Disabled]			
Compliance Test Mode	[Disabled]			
PCIe-USB Glitch W/A	[Disabled]			
PCIe function swap	[Enabled]			
► PCI Express Gen3 Eq Lanes		→←: Select Screen ↑↓: Select Item		
▶ PCIE2 x4 Slot		Enter: Select		
▶ PCIE4 x4 Slot		F1: Genneral Help		
▶ PCIE5 x4 Slot		F2: Previous Values		
► Lan1		F3: Optimized Defaults		
► Lan2		F4: Save & EXIT Esc. Exit		
► PCIE TO PCI				
► PCIE3 x1 Slot				
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4. 以 PCIE2 槽为例,按 Enter 键进入 < PCIE2 x4 Slot>选项,按计键滚动至页面最后一项 < Extra contions>,按 Enter 键进入 选项。

Apt	io Setup Ut Chipset	ility - Copyright (C)2023 American Megatrend	s, Inc.	
PCIE2 x4 Slot Topology ASPM		[Enabled] [Unknown] [Disabled]		
▶ Extra contions		* 按个↓键滚动至页面最后一项 *	 →←: Select Screen ↑↓: Select Item Enter: Select ·/+: Change Opt. F1: Genneral Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit Esc: Exit 	
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5. 选择 < Detect Non-Compliance Device>,按 Enter 键,选择 Enabled,按 Enter。(PCIE2 插槽已经设置为兼容模式)



6. 选择完成后按 F4,选择 Yes,按 Enter 保存退出,设置完毕。



7. 如需恢复 BIOS 默认状态,开机按 Delete 进入 BIOS 设置界面,按 F3 (恢复 BIOS 初始状态),再按 F4 保存退出即可。